BUDDHA INSTITUTE OF TECHNOLOGY, GIDA, GORAKHPUR
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
CLASS TEST-2 (EVEN SEMESTER 2022-23)
MAY-2023

Course:	B.Tech		Semester:	6
Subject:	EMBEDDED SYSTEMS		Subject Code:	KOE-062
M.M.	30	Time:	2Hrs	Roll No._____

SECTION-A

1. Attempt all questions. Each questions carry equal marks.   **Marks: 5*2=10**

Q. No.1	Question	Level of Taxonomy	Course Outcome
a.	Define kernel	Understanding	**CO-4**
b.	How do the following indicate the start and end of a byte or data frames? (a) I2C (b) CAN	Understanding	**CO-3**
c.	What is EDLC?	Understanding	**CO-3**
d.	Define deployment phase.	Understanding	**CO-4**
e.	Define process and threads	Understanding	**CO-4**

SECTION-B

Attempt All questions. Each questions carry equal marks.   **Marks: 3*5= 15**

Q. No.2	Question	Level of Taxonomy	Course Outcome
a.	Explain the terminologies (i) Semaphores (ii) Mail box (iii) Pipes(iv) Shared memory RTOS.	Understanding	**CO-4**
	OR		
a.	Write the difference between process and program in RTOS.	Understanding	**CO-4**
b.	Describe one type of serial communication bus with itscommunication protocol	Understanding	**CO-3**
	OR		
b.	Explain the functionalities of RS 232 and RS 485 standardserial interface with neat diagram.	Understanding	**CO-3**
c	Explain the difference between RISC and CISC processor in detail.	Understanding	**CO-3**
	OR		
c	Mention the essential and objectives of EDLC. Discuss indetail about the different phases of EDLC.	Understanding	**CO-3**

SECTION-C

Attempt All questions. Each questions carry equal marks.   **Marks: 2*5= 10**

Q. No.3	Question	Level of Taxonomy	Course Outcome
a.	Analyze the following modeling of EDLC (i) Linear or Waterfall Model (ii)Iterative or Incremented type of Model (iii)Proto typing or Evolutionary Model (iv) Spiral Model.	Understanding	**CO-5**
	OR		
a.	Illustrate the following type of computational state model (i)Data Flow Graph Type Model (ii) State Diagram Type Model(iii) Concurrent Process Model with suitable example for each..	Understanding	**CO-5**
b	With suitable diagram explain the following Protocol (i) UART Protocol (ii) SPI Protocol (iii)I2C Protocol	Understanding	**CO-4**

Note: Revised Bloom's Taxonomy Levels-

L1->Remembering, L2->Understanding, L3->Applying,
L4->Analyzing, L5->Evaluating, L6->Creating